

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(cam or (content same addressable same memory)) and (fabricating same flash same core same cam) and (cam same flash same accessing)	USPAT; EPO; JPO	OR	OFF	2005/02/25 15:45
L2	0	(cam or (content same addressable same memory)) and (fabricating same flash same cam) and (cam same flash same accessing)	USPAT; EPO; JPO	OR	OFF	2005/02/25 15:46
L3	0	(cam or (content same addressable same memory)) and (fabricating same flash same (cam or (content same addressable same memory))) and ((cam or (content same addressable same memory)) same flash same accessing)	USPAT; EPO; JPO	OR	OFF	2005/02/25 15:46
L4	0	(cam or (content same addressable same memory)) and (flash same (cam or (content same addressable same memory))) and ((cam or (content same addressable same memory)) same flash same accessing)	USPAT; EPO; JPO	OR	OFF	2005/02/25 15:47
L5	70	(cam or (content same addressable same memory)) and (flash same (cam or (content same addressable same memory))) and ((cam or (content same addressable same memory)) same flash same (access or accessing))	USPAT; EPO; JPO	OR	OFF	2005/02/25 15:47
L6	3	(cam or (content same addressable same memory)) and (fabricating same flash same (cam or (content same addressable same memory))) and ((cam or (content same addressable same memory)) same flash same (access or accessing))	USPAT; EPO; JPO	OR	OFF	2005/02/25 15:47